

**Amendments to the Specifications:**

In the DESCRIPTION OF THE PREFERRED EMBODIMENTS, please replace the last paragraph on page 18, which continues as the first paragraph on page 19 with the following paragraph:

**Fig. 15a** shows a realistic circuit diagram of an implementation, in accordance with an embodiment of this invention. **Amp 1 to Amp n** are said operational amplifiers, **Sw 1 to Sw n** are the switching devices and **Cap 1 to Cap n** are said capacitors that will be switched in parallel, resulting in the total capacitance **varCap**. **R1 to Rn** build the resistor chain to produce references voltages for the ~~translinear~~operational amplifiers of each stage, as already shown in **Fig. 9a**.